



IFW

PATENT
0941-0783P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: HU, Shu-Fen et al. Conf.: 2300
Appl. No.: 10/602,890 Group: 2818
Filed: June 25, 2003 Examiner: David Vu
For: SINGLE ELECTRON TRANSISTOR AND FABRICATION METHOD
THEREOF

SMALL ENTITY TRANSMITTAL FORM

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 7, 2004

Sir:

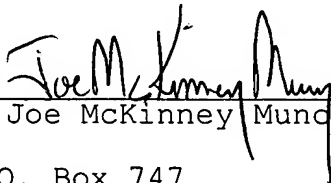
Transmitted herewith is a Reply to Restriction/Election Requirement in the above-identified application.

- ☒ Applicant claims small entity status under 37 C.F.R. § 1.27.
- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of \$0.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. A triplicate copy of this sheet is attached.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Joe McKinney Mundy, #32,334

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

KM/asc
0941-0783P

Attachment(s)

(Rev. 02/08/2004)



PATENT
0941-0783P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: HU, Shu-Fen et al. Conf.: 2300
Appl. No.: 10/602,890 Group: 2818
Filed: June 25, 2003 Examiner: David Vu
For: SINGLE ELECTRON TRANSISTOR AND
FABRICATION METHOD THEREOF

REPLY TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

July 7, 2004

Sir:

Responsive to the Office Action dated June 7, 2004, the following election and remarks are respectfully submitted in connection with the above-identified application.

REMARKS

Claims 1-12 are now pending in the present application.

The Examiner has given the following Restriction Requirement:

Group I, claims 9-12, drawn to a semiconductor device; and

Group II, claims 1-8, drawn to a process of making a semiconductor device.

In response to this requirement, Applicants hereby elect Group I, claims 9-12 drawn to a semiconductor device. This election is with traverse.